L Number	Hits	Search Text	DB	Time stamp
-	10	(parallel$3 same (cpu processor processing) and (dsp (digital adj2 signal adj2 processor)) and (load$3 near3 manag$5) and communicat$3 and transmi$5 and (cpu processor) and ((dma (direct adj2 memory adj2 access)) same register$3) and fifo and (state adj2 machine) and (memor$3 same interfac$3 same (dma (direct adj2 memory adj2 access)) same external) and (control$4 near4 register) and (status near4 register) and (dma (direct adj2 memory adj2 access)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/14 14:45
-	12	(((parallel$3 same (cpu processor processing)) and (dsp (digital adj2 signal adj2 processor)) and (load$3 near3 manag$5) and communicat$3 and transmi$5 and (cpu processor) and ((dma (direct adj2 memory adj2 access)) and register$3) and fifo and (state adj2 machine) and (memor$3 same interfac$3 same (dma (direct adj2 memory adj2 access)) and external) and (control$4 near4 register) and (status near4 register) and (dma (direct adj2 memory adj2 access)))) not "110"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/14 14:59
-	2	(((parallel$3 same (cpu processor processing)) and (dsp (digital adj2 signal adj2 processor)) and (load$3 near3 manag$5) and communicat$3 and transmi$5 and (cpu processor) and ((dma (direct adj2 memory adj2 access)) and register$3) and fifo and (state adj2 machine) and (memor$3 same interfac$3 same (dma (direct adj2 memory adj2 access)) and external) and (control$4 near4 register) and (status near4 register) and (dma (direct adj2 memory adj2 access)))) not (((parallel$3 same (cpu processor processing)) and (dsp (digital adj2 signal adj2 processor)) and (load$3 near3 manag$5) and communicat$3 and transmi$5 and (cpu processor) and ((dma (direct adj2 memory adj2 access)) same register$3) and fifo and (state adj2 machine) and (memor$3 same interfac$3 same (dma (direct adj2 memory adj2 access)) same external) and (control$4 near4 register) and (status near4 register) and (dma (direct adj2 memory adj2 access))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/14 14:59
-	52	(((dma (direct adj2 memory adj2 access)) same register$3) and (fifo same (state adj2 machine)) and (memor$3 same interfac$3 same (dma (direct adj2 memory adj2 access)) and external) and (((cpu processor) same ((control$4 near4 register) and (status near4 register) and (dma (direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2 processor)))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 08:01
-	48	((real-time (real adj2 time)) same (dsp (digital adj2 signal adj2 processor)) same load$3 same manag$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 10:19
-	8	(((real-time (real adj2 time)) same (dsp (digital adj2 signal adj2 processor)) same load$3 same manag$5)) and ((ip and (sonet (synchronous with optical with network))) (connect$3 same (fiber-optic (fiber with optic)) same transmi$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 10:41
-	1	(((((dma (direct adj2 memory adj2 access)) same register$3) and (fifo same (state adj2 machine)) and (memor$3 same interfac$3 same (dma (direct adj2 memory adj2 access)) and external) and (((cpu processor) same ((control$4 near4 register) and (status near4 register) and (dma (direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2 processor)))))) and ((ip and (sonet (synchronous with optical with network))) (connect$3 same (fiber-optic (fiber with optic)) same transmi$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 10:42
-	271	(dsp (digital adj2 signal adj2 processor)) and ((ip and (sonet (synchronous with optical with network))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 10:43
-	37	(dsp (digital adj2 signal adj2 processor)) and ((ip and (sonet (synchronous with optical with network))) and (connect$3 same (fiber-optic (fiber with optic)) same transmi$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 10:59

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-	1	((dsp (digital adj2 signal adj2 processor)) and ((ip and (sonet (synchronous with optical with network))) (connect$3 same (fiber-optic (fiber with optic)) same transmi$5))) and foip	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 11:00
-	1	((((dsp (digital adj2 signal adj2 processor)) and ((ip and (sonet (synchronous with optical with network))) (connect$3 same (fiber-optic (fiber with optic)) same transmi$5))) and foip) and (transmi$5 communicat$3 data medium)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 11:01
-	52	(((dma (direct adj2 memory adj2 access)) same register$3) and (fifo same (state adj2 machine)) and (memor$3 same interfac$3 same (dma (direct adj2 memory adj2 access)) and external) and (((cpu processor) same ((control$4 near4 register) and (status near4 register) and (dma (direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2 processor)))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 10:18
-	0	((((dma (direct adj2 memory adj2 access)) same register$3) and (fifo same (state adj2 machine)) and (memor$3 same interfac$3 same (dma (direct adj2 memory adj2 access)) and external) and (((cpu processor) same ((control$4 near4 register) and (status near4 register) and (dma (direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2 processor)))))) and ((ping-pong (ping adj2 pong)) with memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 10:51
-	52	((real-time (real adj2 time)) same (dsp (digital adj2 signal adj2 processor)) same load$3 same manag$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 10:19
-	3	(((real-time (real adj2 time)) same (dsp (digital adj2 signal adj2 processor)) same load$3 same manag$5)) and ((ping-pong (ping adj2 pong)) with memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 10:20
-	274	((dsp (digital adj2 signal adj2 processor)) same load$3 same manag$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 10:20
-	7	(((dsp (digital adj2 signal adj2 processor)) same load$3 same manag$5)) and ((ping-pong (ping adj2 pong)) with memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/08 10:17
-	4	((((dsp (digital adj2 signal adj2 processor)) same load$3 same manag$5)) and ((ping-pong (ping adj2 pong)) with memory)) not ((((real-time (real adj2 time)) same (dsp (digital adj2 signal adj2 processor)) same load$3 same manag$5)) and ((ping-pong (ping adj2 pong)) with memory))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 10:20
-	134	(dsp (digital adj2 signal adj2 processor)) and ((ping-pong (ping adj2 pong)) with memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 10:53
-	0	(((((dma (direct adj2 memory adj2 access)) same register$3) and (fifo same (state adj2 machine)) and (memor$3 same interfac$3 same (dma (direct adj2 memory adj2 access)) and external) and (((cpu processor) same ((control$4 near4 register) and (status near4 register) and (dma (direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2 processor)))))) and ((ping-pong (ping adj2 pong)) with buffer$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 11:06
-	1	(((((dma (direct adj2 memory adj2 access)) same register$3) and (fifo same (state adj2 machine)) and (memor$3 same interfac$3 same (dma (direct adj2 memory adj2 access)) and external) and (((cpu processor) same ((control$4 near4 register) and (status near4 register) and (dma (direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2 processor)))))) and (thread$3 with memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 10:52

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-	44	((((dsp (digital adj2 signal adj2 processor)) same load$3 same manag$5)) and (thread$3 with memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 13:20
-	7	(dsp (digital adj2 signal adj2 processor)) and (MUX near5 (external near5 memory))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 11:16
-	241	(dsp (digital adj2 signal adj2 processor)) and (MUX near5 memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 11:19
-	138	(dsp (digital adj2 signal adj2 processor)) and (MUX same memory same external)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 11:20
-	27	((((dsp (digital adj2 signal adj2 processor)) same load$3 same manag$5)) and (thread$3 with memory)) and (mac ((memory with access with control$4) same array) or (memory with (control$4 manag$5) same external))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/08 09:54
-	26	((((dsp (digital adj2 signal adj2 processor)) same load$3 same manag$5)) and (thread$3 with memory)) and ((mac (memory with access with control$4)) and external)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 14:25
-	33	(((real-time (real adj2 time)) same (dsp (digital adj2 signal adj2 processor)) same load$3 same manag$5)) and ((mac (memory with access with control$4)) and external)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 14:25
-	33	((dsp (digital adj2 signal adj2 processor)) same load$3 same (balanc$3 manag$5)) and (parallel$3 with (schedul$3 process$3)) and ((dma (direct adj2 memory adj2 access$3)) same (queu$3 fifo first-in-first-out (first adj in adj first adj out)) same (register$3 (high near3 speed$3 near3 (storage memor$3))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/08 13:23
-	14	(("6104721" "4991169" "5724587" "5440740" "5842014" "5291614" "5822606") .pn.) and ((parallel$5 with process$3) (DMa (direct near3 memory near3 access$3)) register$3 (queu$3 fifo first-in-first-out (first adj in adj first adj out)) (cpu dsp (central adj process$3 adj unit) (digital adj signal adj process$3)) ((stat$3 with machine)) and interfac$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/21 08:40
-	6	(("6104721" "4991169" "5724587" "5440740" "5842014" "5291614" "5822606") .pn.) and (external)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 14:55
-	173	((DMa (direct near3 memory near3 access$3)) same register$3 same (queu$3 fifo first-in-first-out (first adj in adj first adj out)) same (stat$3 with machine))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 16:55